

Programmable Logic Devices

ONE MARK QUESTIONS

- 12.1 A PLA can be used to realize a combinational logic. So, option 'c' is correct.
- 12.2 We know that PROM contains a fixed AND array and a programmable OR array whereas, PLA contains a programmable AND array and a programmable OR array. So, both options 'c' and 'd' are correct.

TWO MARKS QUESTION

- 12.1 We know that inputs to AND gate must be high for the generation of a minterm. So, looking at the connections, we can write $F = \bar{P}\bar{Q}R + \bar{P}QR + P\bar{Q}R$. So, option 'a' is correct.